



Sheet 1 of 1

Form 1449*

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INFORMATION DISCLOSURE STATEMENT
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Applicant: Eric T. Stubbs

Filing Date: January 28, 2000

Group: 2734

U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<i>K</i>	4,156,197	05/22/1979	Merrell, R.G.	325	421	12/06/77
<i>K</i>	5,506,878	04/09/1996	Chiang, D.	377	39	07/18/94
<i>K</i>	5,694,068	12/02/1997	Rokugo, Y.	327	159	04/25/96
<i>K</i>	5,764,300	06/09/1998	Badger, D.M.	348	536	07/20/95
<i>K</i>	5,771,264	06/23/1998	Lane, C.	375	376	01/07/97
<i>K</i>	5,790,612	08/04/1998	Chengson, D.P., et al.	375	393	02/29/96
<i>K</i>	5,809,064	09/15/1998	Fenton, P., et al.	375	208	08/02/96
<i>K</i>	5,815,016	09/29/1998	Ericksons, C.R.	327	158	06/14/96
<i>K</i>	5,818,365	10/06/1998	Hush, G., et al.	341	100	10/01/96
<i>K</i>	5,854,615	12/29/1998	Hush, G.E.	345	99	10/03/96
<i>K</i>	5,920,518	07/06/1999	Harrison, R.M., et al.	365	233	02/11/97
<i>K</i>	5,923,611	07/13/1999	Ryan, K.J.	365	233	12/20/96
<i>K</i>	5,926,047	07/20/1999	Harrison, R.M.	327	159	08/29/97
<i>K</i>	5,946,244	08/31/1999	Manning, T.A.	365	194	03/05/97
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**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
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OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

**Examiner Initial	
<i>K</i>	Lin, F., et al., "A Register Controlled Symmetrical DLL for Double-Data-Rate DRAM", <u>IEEE Journal of Solid State Circuits</u> , 34(4), pp. 565-568, (April 1999)

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Examiner

J. Levin

Date Considered

5/29/2003

*Substitute Disclosure Statement Form (PTO-1449)

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